Product data sheet

1. General description

WG50S65HAW1 uses advanced Fine Trench Field-stop IGBT technology with antiparallel diode in TO247 package to provide extremely low $V_{\text{CE(sat)}}$, and excellent switching performance. This device offers Best-in-Class efficiency in hard switching and resonant topology.



2. Features and benefits

- · Maximum junction temperature 175 °C
- · Positive Temperature efficient for easy paralleling
- · Very soft, fast recovery anti-parallel diode
- · High switching speed
- · EMI Improved Design

3. Applications

- PFC
- Solar converters
- UPS
- Welding Converters
- · Mid to high range switching frequency converters

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter				Value		Unit	
V _{CE}	Collector-emitter voltage, T _j ≥ 25 °C			650			V	
I _C	DC collector current, limited by $T_{j(max)}$ $T_C = 100 ^{\circ}C$				50		A	
Symbol	Parameter Conditions		Notes	Min	Тур	Max	Unit	
Static cha	Static characteristics							
V _{CE(sat)}	Collector-emitter saturation voltage	$V_{GE} = 15 \text{ V}; I_{C} = 50 \text{ A}; T_{j} = 25 \text{ °C}$		-	2.0	2.5	V	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		•C
2	С	collector		
3	E	emitter		
mb	С	mounting base; connected to collector	TO247	G E sym200

6. Ordering information

Table 3. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Package version	Package issue date
WG50S65HAW1	TO247	WG50S65HAW1Q	Tube	30	SOT429	25-Mar-2013

7. Marking

Table 4. Marking codes

Type number	Marking codes
WG50S65HAW1	G50S65 HAW1

8. Limiting values

Table 5. Limiting values

Symbol	Parameter	Notes	Value	Unit
V _{CE}	Collector-emitter voltage, T _j ≥ 25 °C		650	V
I _c	DC collector current, limited by $T_{j(max)}$ T_{c} = 25 °C T_{c} = 100 °C		76 50	А
I _{C(puls)}	Pulsed collector current, t _p limited by T _{j(max)}		100	А
-	Turn off safe operating area $V_{CE} \le 650 \text{ V}, T_j \le 175 ^{\circ}\text{C}, t_p = 1 \mu\text{s}$		100	А
l _F	Diode forward current, limited by $T_{j(max)}$ T_{c} = 25 °C T_{c} = 100 °C		40 20	А
I _{Fpuls}	Diode pulsed current, t _p limited by T _{j(max)}		60	А
V_{GE}	Gate-emitter voltage		±20	V
P _{tot}	Power dissipation $T_C = 25 ^{\circ}\text{C}$ Power dissipation $T_C = 100 ^{\circ}\text{C}$		312 156	W
T _{stg}	Storage temperature		-55 to +150	°C
T_{jmax}	Maximum operating junction temperature		175	°C
-	Peak soldering temperture		260	°C
M	Mounting Torque with washer		0.55	Nm

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
R _{th(j-c)}	IGBT thermal resistance from junction to case			-	0.48	-	K/W
R _{th(j-c)}	Diode thermal resistance from junction to case			-	2	-	K/W
R _{th(j-a)}	thermal resistance from junction to ambient			-	40	-	K/W

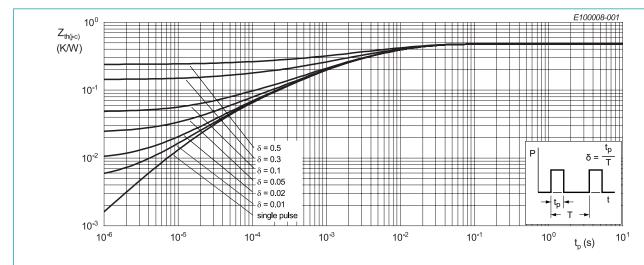


Fig. 1. Transient thermal impedance from junction to case as a function of pulse duration; IGBT

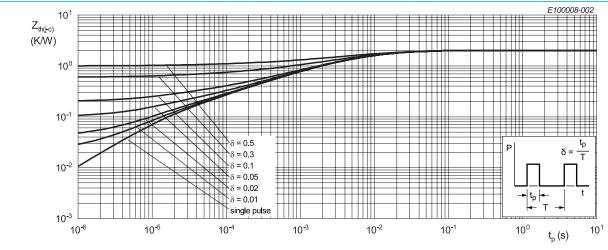


Fig. 2. Transient thermal impedance from junction to case as a function of pulse duration; Diode

IGBT

10. Characteristics

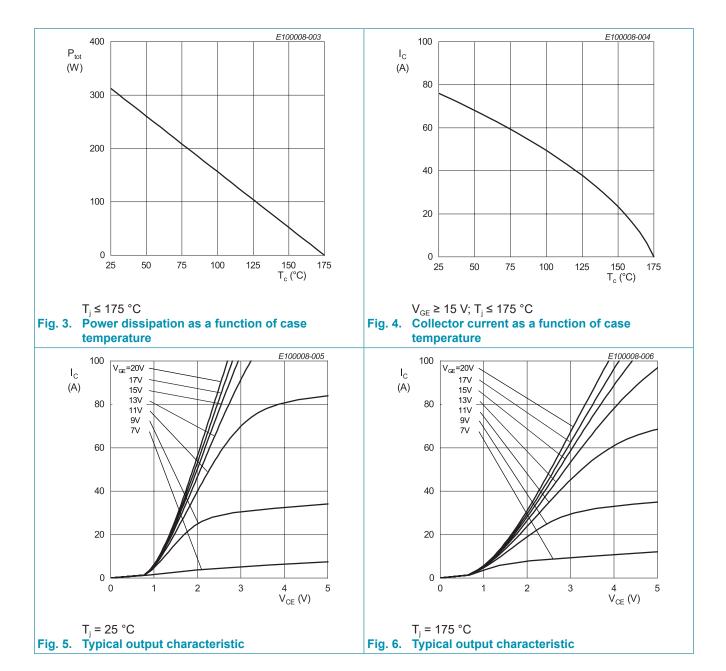
Table 7. Characteristics

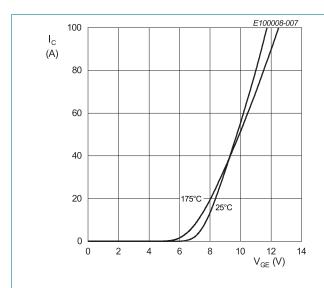
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
Static cha	racteristics						
BV _{CES}	Collector-emitter breakdown voltage	$V_{GE} = 0 \text{ V}; I_{C} = 1.0 \text{ mA}$		650	-	-	V
$V_{\text{CE(sat)}}$	Collector-emitter saturation	V_{GE} = 15 V; I_{C} = 50 A; T_{j} = 25 °C		-	2.0	2.5	V
	voltage	V_{GE} = 15 V; I_{C} = 50 A; T_{j} = 175 °C		-	2.7	-	V
V_{F}	Diode forward voltage	$V_{GE} = 0 \text{ V}; I_F = 20 \text{ A}; T_j = 25 \text{ °C}$		-	2.3	-	V
		V _{GE} = 0 V; I _F = 20 A; T _j = 175 °C		-	1.9	-	V
$V_{\text{GE(th)}}$	Gate-emitter threhold voltage	$I_{\rm C}$ = 0.5 mA; $V_{\rm CE}$ = $V_{\rm GE}$		4.3	5.4	6.5	V
I _{CES}	Zero gate voltage collector current	$V_{CE} = 650 \text{ V}; V_{GE} = 0 \text{ V}; T_j = 25 \text{ °C}$		-	-	100	μA
		$V_{CE} = 650 \text{ V}; V_{GE} = 0 \text{ V}; T_j = 175 ^{\circ}\text{C}$		-	-	1	mA
g _{fs}	Transconductance	V _{CE} = 20 V; I _C = 50 A		-	23	-	S
Dynamic	characteristics						
C _{ies}	Input capacitance	$V_{CE} = 30 \text{ V}; V_{GE} = 0 \text{ V}; f = 1 \text{ MHz};$		-	1593	-	pF
C _{oes}	Output capacitance	T _j = 25 °C		-	45	-	pF
C _{res}	Reverse transfer capacitance			-	18	-	pF
Q_{G}	Gate charge	$V_{CC} = 520 \text{ V}; I_C = 50 \text{ A}; V_{GE} = 15 \text{ V};$ $T_j = 25 \text{ °C}$		-	74	-	nC

11. Switching Characteristics

Table 8. Switching Characteristics, Inductive Load

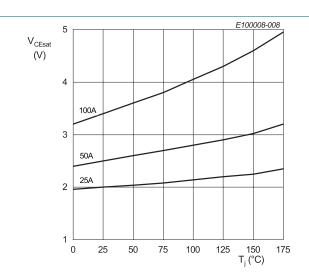
Symbol	Parameter	Conditions	Notes	Min	Тур	Max	Unit
IGBT cha	racteristics						
$t_{d(on)}$	Turn-on delay time	T _j = 25 °C;		-	38	-	nS
t _r	Rise time	$V_{CC} = 400 \text{ V}; I_C = 50 \text{ A}; V_{GE} = 15 \text{V} / 0 \text{V};$ $R_G = 10 \Omega$		-	56	-	nS
$t_{\text{d(off)}}$	Turn-off delay time			-	126	-	nS
t _f	Fall time			-	31	-	nS
E _{on}	Turn-on energy			-	1.5	-	mJ
E _{off}	Turn-off energy			-	0.65	-	mJ
E _{ts}	Total switching energy			-	2.15	-	mJ
t _{d(on)}	Turn-on delay time	$T_{j} = 175 ^{\circ}\text{C};$ $V_{CC} = 400 \text{V}; I_{C} = 50 \text{A}; V_{GE} = 15 \text{V} / 0 \text{V};$ $R_{G} = 10 \Omega$		-	34	-	nS
t _r	Rise time			-	57	-	nS
$t_{\text{d(off)}}$	Turn-off delay time			-	138	-	nS
t _f	Fall time			-	34	-	nS
E _{on}	Turn-on energy			-	2.25	-	mJ
E _{off}	Turn-off energy			-	0.85	-	mJ
E _{ts}	Total switching energy			-	3.1	-	mJ
Diode cha	aracteristics						
t _{rr}	Reverse recovery time	T _j = 25 °C;		-	42	-	nS
Q _r	Reverse recovery charge	$V_R = 400 \text{ V}; I_F = 20 \text{ A}; dI_F/dt = 500 \text{A/us}$		-	231	-	nC
I _{RM}	Reverse recovery peak current			-	10	-	А
t _{rr}	Reverse recovery time	$T_j = 175 ^{\circ}\text{C};$ $V_R = 400 ^{\circ}\text{V}; I_F = 20 ^{\circ}\text{A}; dI_F/dt = 500 ^{\circ}\text{A/us}$		-	90	-	nS
Q _r	Reverse recovery charge			-	733	-	nC
I _{RM}	Reverse recovery peak current			-	15	-	А





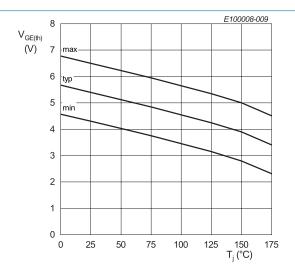
 V_{CE} = 20 V

Fig. 7. Typical transfer characteristic



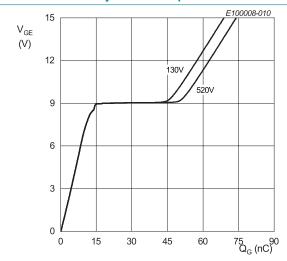
 $V_{GE} = 15 V$

Fig. 8. Typical collector-emitter saturation voltage as a function of junction temperature



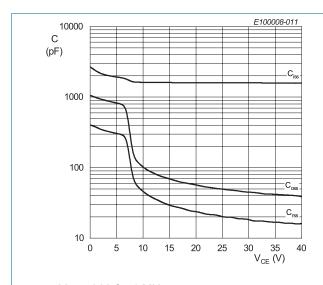
 $I_{c} = 500 \, \mu A$

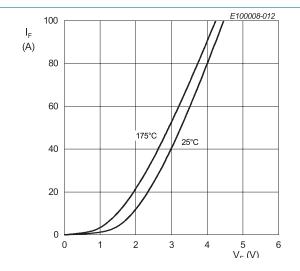
Fig. 9. Gate-emitter threshold voltage as a function of junction temperature



 $I_{c} = 50 \text{ A}$

Fig. 10. Typical gate charge

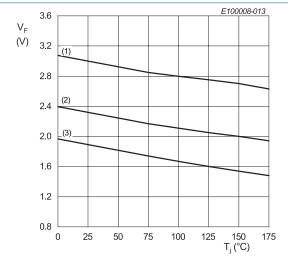


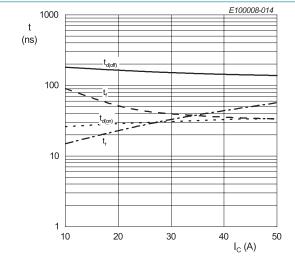


 $V_{GE} = 0 \text{ V; } f = 1 \text{ MHz}$

Fig. 11. Typical capacitance as a function of collector-emitter voltage

Fig. 12. Typical diode forward current as a function of forward voltage





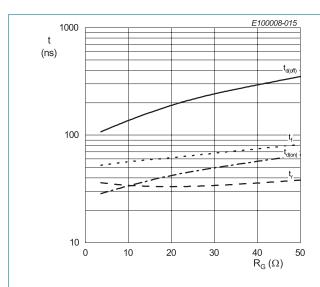
(1) $I_F = 40 \text{ A}$ (2) $I_F = 20 \text{ A}$

 $(3) I_F = 10 A$

 R_g = 10 Ω ; V_{GE} = 15V/0V; T_j = 175 °C; V_{CE} = 400 V; inductive load

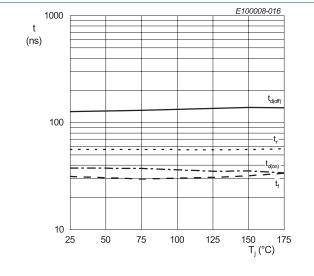
Fig. 13. Typical diode forward voltage as a function of junction temperature

Fig. 14. Typical switching times as a function of collector current



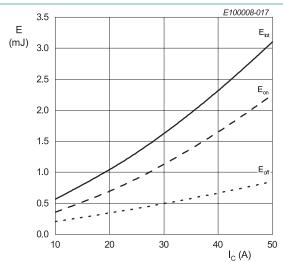
 I_C = 50 A; V_{GE} = 15V/0V; T_j = 175 °C; V_{CE} = 400 V; inductive load

Fig. 15. Typical switching times as a function of gate resistance



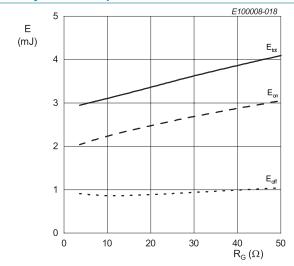
 I_{C} = 50 A; V_{GE} = 15V/0V; R_{g} = 10 Ω ; V_{CE} = 400 V; inductive load

Fig. 16. Typical switching times as a function of junction temperature



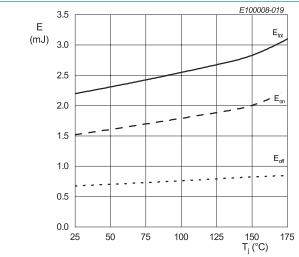
 R_g = 10 Ω ; V_{GE} = 15V/0V; T_j = 175 °C; V_{CE} = 400 V; inductive load

Fig. 17. Typical switching energy losses as a function of collector current



 I_{C} = 50 A; V_{GE} = 15V/0V; T_{j} = 175 °C; V_{CE} = 400 V; inductive load

Fig. 18. Typical switching energy losses as a function of gate resistance



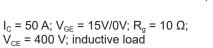


Fig. 20. Forward bias safe operating area



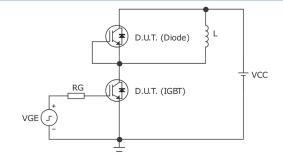


Fig. 21. Test circuit for inductive load switching

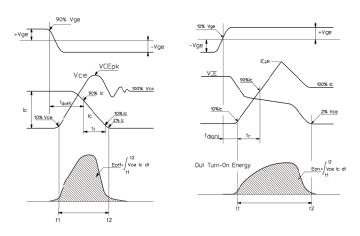
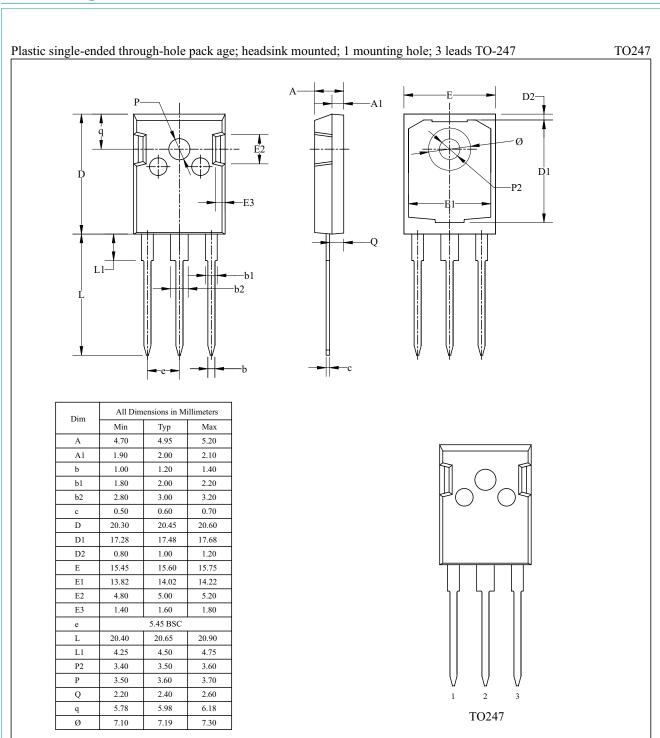


Fig. 22. Definition of switching times and losses

12. Package outline



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13. Legal information

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Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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